



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2826
Examiner: Tuan N. Quach
Confirmation No.: 6985

In Re PATENT APPLICATION Of:

Applicant: Tadashi Yamaguchi

)

Serial No.: 10/748,258

) R.C.E.

Filed: December 31, 2003

) AMENDMENT

For: SEMICONDUCTOR DEVICE AND
METHOD FOR FABRICATING
SEMICONDUCTOR DEVICE

) and

) PETITION

Attny Ref.: OKI 396

)

November 13, 2005

Mail Stop Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is in response to the Final Official Action mailed on July 13, 2005. Payment for one month's extension of time is attached. An RCE is filed on even date herewith.

Please charge our Deposit Account No. 18-0002 if any additional fees are needed to enter this paper, and please advise us accordingly. It is noted that no petition is required because of the authorization to charge, but this paper is a petition for extension of time.

Claims 12 and 13 are supported at page 10, lines 13-17 (also, "separated" chips are disclosed at the bottom of page 11). Claim 14 is supported on pages 9-10, where the words "next" and "subsequently" are repeated at various places. Claim 15 is supported at page 11, line 1, reciting that the conductor 24 buries (fills) the through hole 22. Claim 16 is supported at the top of page 13.